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UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S)	John M. Grant	GROUP ART UNIT:	2826
APPLN. NO.:	10/677,573	EXAMINER:	Minh Loan Tran
FILED:	October 2, 2003		
TITLE:	SEMICONDUCTOR DEVICE INCORPORATING A DEFECT CONTROLLED STRAINED CHANNEL STRUCTURE AND METHOD OF MAKING SAME		

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transmitted to the Patent and Trademark Office on  
January 3, 2005

*Elaine Cox*

Elaine Cox

AMENDMENT

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Responsive to the Office Action dated November 3, 2004, and Examiner's comments with regard thereto, please enter the following amendments in the above-entitled application, without prejudice or disclaimer.

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper.

**Remarks/Arguments** begin on page 7 of this paper.

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**FAX**

Law Department  
7700 West Parmer Lane  
Mail Drop PL02  
Austin, Texas 78729

Telephone: 512.996.6839

Facsimile: 512.996.6853

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Number of Pages (including this page)

Date: January 3, 2005  
To: Examiner Minh Loan Tran - Group Art Unit 2826  
Location: United States Patent and Trademark Office  
Fax No.: 703.872.9306  
From: Michael J. Balconi-Lamica 34,291  
Subject: 10/677,573 John M. Grant

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**MESSAGE:**

Enclosed herewith, please find a Response to Office Action mailed November 3, 2004 for filing in the below-identified application.

**Paid by Deposit Account: 503079, Freescale Semiconductor . \$0.00**

**PLEASE GIVE THESE PAPERS TO:**

EXAMINER: Minh Loan Tran  
GROUP ART UNIT: 2826  
SERIAL NO.: 10/677,573  
FILED: OCTOBER 2, 2003  
INVENTOR: JOHN M. GRANT